

L Number	Hits	Search Text	DB	Time stamp
1	1422144	memory cell\$1 and (eras\$4 with rewrit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 13:56
2	2979206	writ\$3 with predetermined logical value\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 13:56
3	567617	(memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:00
4	1967002	reading out near1 writtern logical value\$1 near2 memory cell\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:01
5	338229	((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:02
6	1017682	compar\$3 and (writtern logical value\$1 with read-out logical value\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:02
7	199841	(((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:03
8	2366221	discontinu\$3 near1 test with failure memory cell\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:07
9	199841	(((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1))) and (discontinu\$3 near1 test with failure memory cell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:08
10	274	repair\$3 and (memory with impossible)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:08
11	809050	uncorrectable error\$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:08
12	1423214	uncorrectable memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:08

13	1975440	(uncorrectable error\$) or (uncorrectable memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:09
14	274	(repair\$3 and (memory with impossible)) and ((uncorrectable error\$) or (uncorrectable memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:10
15	156	((repair\$3 and (memory with impossible)) and ((uncorrectable error\$) or (uncorrectable memory))) and (((((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1))) and (discontinu\$3 near1 test with failure memory cell\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:11
16	4375786	substitut\$3 with spare memory component\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:11
17	156	((((repair\$3 and (memory with impossible)) and ((uncorrectable error\$) or (uncorrectable memory))) and (((((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1))) and (discontinu\$3 near1 test with failure memory cell\$1))) and (substitut\$3 with spare memory component\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:12
18	5887707	number near1 failure memory cell\$1 with reach\$2 predetermined number	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:13
19	156	((((repair\$3 and (memory with impossible)) and ((uncorrectable error\$) or (uncorrectable memory))) and (((((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1))) and (discontinu\$3 near1 test with failure memory cell\$1))) and (substitut\$3 with spare memory component\$1)) and (number near1 failure memory cell\$1 with reach\$2 predetermined number))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:17
20	153032	erased en bloc	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:18
21	35	((((((repair\$3 and (memory with impossible)) and ((uncorrectable error\$) or (uncorrectable memory))) and (((((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1))) and (discontinu\$3 near1 test with failure memory cell\$1))) and (substitut\$3 with spare memory component\$1)) and (number near1 failure memory cell\$1 with reach\$2 predetermined number)) and (erased en bloc))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:19
22	4378	714/718.ccls. or 714/719.ccls. or 714/710.ccls.or 714/704.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:20

23	7	(714/718.ccls. or 714/719.ccls. or 714/710.ccls.or 714/704.ccls.) and (((((repair\$3 and (memory with impossible)) and ((uncorrectable error\$) or (uncorrectable memory)))) and (((((memory cell\$1 and (eras\$4 with rewrit\$4)) and (writ\$3 with predetermined logical value\$1)) and (reading out near1 writtern logical value\$1 near2 memory cell\$1)) and (compar\$3 and (writtern logical value\$1 with read-out logical value\$1))) and (discontinu\$3 near1 test with failure memory cell\$1))) and (substitut\$3 with spare memory component\$1)) and (number near1 failure memory cell\$1 with reach\$2 predetermined number)) and (erased en bloc))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:21
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